

Electronic Acknowledgement Receipt

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Application Number:	10712523
International Application Number:	
Confirmation Number:	8679
Title of Invention:	System for substrate potential regulation during power-up in integrated circuits
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Deposit Account					
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File Listing:					
Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Drawings-only black and white line drawings	TRAN-P196_ReplacementSheet_5-23-2008.pdf	8729	no	1
			ca041d6d0e5e592b5061428c34cfa7e881145c9		
Warnings:					
Information:					
2		TRAN-P196_Amdt_5-23-2008.pdf	176518	yes	9
			4b07059e4f5968db3a65479804013a5eae189c9e		
	Multipart Description/PDF files in .zip description				
	Document Description		Start	End	
	Amendment - After Non-Final Rejection		1	1	
	Specification		2	3	
	Applicant Arguments/Remarks Made in an Amendment		4	9	
Warnings:					
Information:					
3	Fee Worksheet (PTO-06)	fee-info.pdf	8184	no	2
			003839a6f25d9dfe4eb72bf6bda0c0c71e423324		
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Total Files Size (in bytes):			193431		

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National Stage of an International Application under 35 U.S.C. 371

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New International Application Filed with the USPTO as a Receiving Office

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